

Roll No. \_\_\_\_\_

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**SVS-N-323 B-19**  
**B.Sc. V Semester Degree Examination**  
**ELECTRONICS**  
**Microprocessor and Interfacing**  
**Paper - 5.2**  
**(New)**

**Time : 3 Hours**

**Maximum Marks : 80**

*Instructions to Candidates:*

1. Answer ALL questions from Section - A.
2. Answer any FIVE questions from Section - B.
3. Answer any FOUR questions from Section - C.

**SECTION - A**

1. Choose the correct answer:

(5×1=5)

- i) Which device can store more data?
  - a. CD
  - b. DVD
  - c. Hard disk
  - d. None
- ii) Microprocessor 8085 is ----- bit processor.
  - a. 4
  - b. 8
  - c. 16
  - d. 32
- iii) CMA is ----- byte instruction.
  - a. 1
  - b. 2
  - c. 3
  - d. 4
- iv) ----- flag is affected in case of rotate group of instruction
  - a. Sign
  - b. Carry
  - c. Parity
  - d. Zero
- v) IC 8251 is -----IC.
  - a. USART
  - b. DMA controller
  - c. PPI
  - d. Keyboard display Interface

2. Fill in the blanks: (5×1=5)
- i) The content of UVEPROM can be erased by exposing it to -----.
  - ii) Microprocessor 8085 has ----- flags.
  - iii) DAA stands for -----.
  - iv) ----- and ----- instructions are used to execute subroutine programs.
  - v) PPI has three ----- bit ports.
3. State True or false: (5×1=5)
- i) The address bus in 8085  $\mu p$  is bidirectional.
  - ii) NOP instruction is used to stop the program.
  - iii) Stack works on LIFO principle.
  - iv) TRAP interrupt has highest priority.
  - v) Intel 8253 has 40 pins.

**SECTION - B**

(5×5=25)

4. With a neat circuit diagram explain EE PROM.
5. Explain registers of 8085.
6. Explain addition group of instructions with example.
7. Explain PUSH and POP instructions.
8. Write an ALP to add two 8-bit numbers and store the result in memory location 8500H.
9. Explain the instructions :
  - a) IN address (8 - bit)
  - b) OUT address (8 - bit)
10. Write the characteristics of 8251.

**SECTION - C**

**(4×10=40)**

11. Draw the block diagram of 8085  $\mu p$  and explain each block.
12. Explain the following instructions with example
  - a) LXI H address (16 bit)
  - b) CMC
  - c) Mov A, B
  - d) SUB B
  - e) LDA address (16bit)
13. Draw the timing diagram for execute operation and explain it.
14. Write an ALP to find product of two 8 - bit numbers, the multiplier is stored in the memory location 8500H & the multiplicand is stored in the memory location 8501H. The result shall be stored in memory location 8502H and 8503H.
15. Draw the block diagram of 8255 and explain the operation of each block.
16.
  - a) Write a note on interrupts in 8085.
  - b) Explain branch group of instructions.

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