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SVS 339 B-2K12
B.Sc. Vth Semester Degree Examination
Electronics Optional
Digital Electronics and Microprocessor
Paper - 5.2

Time : 3 Hours

Maximum Marks : 80

Instructions to Candidates.

- 1) Answer **all** questions from Section- A
- 2) Answer any **five** question from Section -B
- 3) Answer any **four** question from Section - C

Section - A

1. Choose the correct answer.

(5X1=5)

- i) A Demultiplexer has
 - a) Many inputs and one output.
 - b) One input and many outputs
 - c) Many inputs and many outputs
 - d) None of the above
- ii) A read/write memory is also known as
 - a) RAM
 - b) ROM
 - c) PROM
 - d) EPROM
- iii) Width of the data bus in intel 8085 microprocessor is
 - a) 4-bit
 - b) 8-bit
 - c) 16-bit
 - d) 32-bit

- iv) An instruction cycle consists of
 - a) Fetch cycle
 - b) Execute cycle
 - c) Both (a) & (b)
 - d) None of the above
- v) TRAP Interrupts has the.....priority.
 - a) Lowest
 - b) Highest
 - c) Both (a) and (b)
 - d) None of the above

2. Fill in the blanks. (1X5=5)

- i) A/D converter is most popular A/D converter which take much shorter time.
- ii) In dynamic RAM memories, the information is stored in
- iii) is the heart of micro computer.
- iv) STACK operation is performed using and instruction.
- v) The operating modes of Intel 8255 programmable peripheral interface (PPI) is types.

3. State whether the following statements are true or false. (1X5=5)

- i) R-2R ladder network is used to convert analog to digital.
- ii) Intel 8085 microprocessor has six 8-bit general purpose registers.
- iii) The instruction of the logical group perform ADD, ADI, SUB, SBT etc.
- iv) RST 7.5 is a maskable interrupt
- v) 64 K byte is equal to 65536 bits.

Section - B

Answer any five questions of the following : (5X5=25)

- 4. What is a demultiplexer? With a neat diagram explain the working of 1 to 4 demultiplexer.
- 5. Write a short note on PROM.
- 6. What are the instruction format are present in 8085 microprocessor. Explain with suitable examples for each format.
- 7. Explain the classification of addressing modes in 8085 microprocessor.
- 8. Explain subroutine.
- 9. Write an assembly language program to add two 8-bit numbers.
- 10. Draw the labelled block diagram of 8257 direct memory access (DMA) controller.

Section - C

Answer any **four** questions.

(4X10=40)

11. With a neat diagram explain A/D conversion by successive approximation method.
12. Briefly explain the working of static and dynamic RAM's.
13. Write a pin out and signals of Intel 8085 microprocessor with their usual meanings.
14. What are logical group instruction? Explain any five logical group instruction of 8085 microprocessor.
15. Explain the following instruction.
 - i) MVI A, data
 - ii) ADI data
 - iii) STA address
 - iv) ORAB
 - v) XCHG
16. Write a short note on
 - i) Registers of 8085 μP .
 - ii) Flags used in 8085 μP .

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